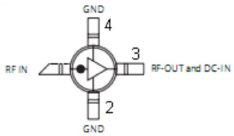
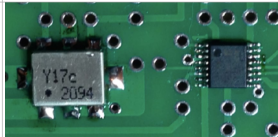




23cm NBFM-Transceiver

Source:	/Users/wmauser/Documents/CAD/KiCad/23cmTrx/23cmTrx.sch					
Date:	2020 October 23, Friday 13:03:43					
Tool:	Eeschema (5.1.6-0-10_14)					
Generator:	/private/var/folders/sl/fcs_h2j6kjch0281dbkx2b80000gn/T/AppTranslocation/9D6954F5-0BDB-4A45-99DC-36F4701EBBD2/d/kicad.app/Contents/SharedSupport/plugins/bom_csv_grouped_by_value.py					
Component Count:	180					
Individual Components:						
Item	Qty	Reference(s)	Value	Type	Remark	Datasheet
		C1	47			~
		C2	1n		parallel zu C3 auf Rückseite assemblymanual4.0.pdf	~
		C3	1n			~
		C4	0,5-5p	Amidon MX5, Sky-Trimmer grün		amidon.de
		C5	0,5-5p	Amidon MX5, Sky-Trimmer grün		amidon.de
		C6	0,5-5p	Amidon MX5, Sky-Trimmer grün		amidon.de
		C7	1n			~
		C8	10p			~
		C9	56p			~
		C10	1p			~
		C11	15p			~
		C12	15p			~
		C13	10n			~
		C14	10n			~
		C15	100			~
		C16	120			~
		C17	47			~
		C18	100n			~
		C19	100n			~
		C20	100n			~
		C21	100n			~
		C22	100n			~
		C23	100n			~
		C24	33n			~
		C25	100n			~
		C26	100n			~
		C27	220n			~
		C28	220u			~
		C29	47n			~
		C30	100n			~
		C31	100n		Vergrößern?	~
		C32	1u		Bessere Modulation	~
		C33	100n			~
		C34	100n			~
		C35	10n			~
		C36	1n			~
		C37	680n			~
		C38	1n			~
		C39	47u			~
		C40	1n			~
		C41	10n			~
		C42	10n			~
		C43	47u			~
		C44	1n			~
		C45	1n			~
		C46	100n			~
		C47	47u			~
		C48	1n			~
		C49	1n			~
		C50	10n			~
		C51	47u			~
		C52	100n			~
		C53	1n			~
		C54	1n			~
		C55	47			~
		C56	1n			~
		C57	47			~
		C58	1n			~
		C60	see text		8 x 8 mm Cu-Folie zwischen dem FET T6 und C62 auf der Leiterbahn isoliert, ggf, Amidon MX5	amidon.de
		C61	1n			~
		C62	47			~
		C63	1n			~

	C64	47			~
	C65	100n			~
	C66	47u			~
	C67	100n			~
	C68	47u			~
	C69	100n			~
	C70	100n			~
	C71	3,3n			~
	C72	3,3n			~
	C73	3,3n			~
	C74	10n			~
	D1	BAT63-03W	SMD, 0805		~
	D2	BAT63-03W	SMD, 0805		~
	D3	Z5v6			~
	D4	Z5V6			~
	D5	C3V3			~
	D6	1N4148			~
	D7	1N4148			~
	D8	1N4148			~
	F1	SFE10,7MT			
	F2	CFU455D			
	IC1	MAR-8		4: https://www.dl6fz.info/23cm/building-a-23cm-nbfm-transceiver/	
	IC2	IAM-81008, M8101620	SMD		kein Datenblatt für M8101620
	IC3	80MHz			
	IC4	MC3362			
	IC5	LM386-3			
	IC6	LM741			http://www.ti.com/lit/ds/symlink/lm741.pdf
	IC7	VCO	SMD		
		ALPS1679C / Y17c-2094-VCO		https://www.dl6fz.info/23cm/building-a-23cm-nbfm-transceiver/ Einbau VCO: https://github.com/realoffie/23CM-TRX/tree/master/modifications#standard-12mhz-oscillator-will-not-start	
		VCO ALPS E-994A (7623A)		email Sandro, DG7SB https://www.rf-microwave.com/resources/products_attachments/5a4386d284433.pdf	
	IC8	ADF4153	SMD	http://www.pe1jpd.nl/index.php/23cm-nbfm-via-ac-92/	
	IC9	TCXO-13MHz G300-Y17		email Sandro, DG7SB	
	IC10	L78L09			http://www.st.com/content/ccc/resource/technical/document/datasheet/15/55/e5/aa/23/5b/43/fd/CD00000446.pdf/files/CD00000446.pdf/jcr:content/translations/en.CD00000446.pdf
	IC11	LM7805			http://www.fairchildsemi.com/ds/LM/LM7805.pdf
	IC12	ATMega328P			
	IC13	MAR-8			
	IC14	AH-1	SMD		
	IC15	78L33		keine Referenz auf PCB	http://www.ti.com/lit/ds/symlink/lm3480.pdf
	IC16	LCD		LCD 2x16 oder LCD 4x20	http://www.newhavendisplay.com/specs/NHD-0420H1Z-FSW-GBW-33V3.pdf
	J1	AVR-ISP-6			~
	L1	8wnd-1,5mm	CuL 0,5 mm		~
	L2	300nH			
	L3	300nH			
	L4	455kHz		unterschiedliche Pinanordnung!	
	L5	8wnd-1,5mm	CuL 0,5 mm		~
	L6	8wnd-1,5mm	CuL 0,5 mm		~
	LS1	Speaker			~
	MK1	Microphone			~
	P1	100k			~
	P2	50k			~
	P3	100k			~
	P4	25k			~
	R1	10	SMD, 0805		~

		R2	150			~
		R3	470			~
		R4	100	SMD, 0805		~
		R5	68	SMD, 0805		~
		R6	100	SMD, 0805		~
		R7	47			~
		R8	47			~
		R9	1k			~
		R10	1k			~
		R11	33k			~
		R12	10k			~
		R13	10k			~
		R14	10			~
		R15	12			~
		R16	150k			~
		R17	150k			~
		R18	1k5			~
		R19	15k			~
		R20	2M2			~
		R22	1k			~
		R23	1k			~
		R24	100k		http://www.pe1jcd.nl/index.php/23cm-nbfm-via-ac-92/	~
		R26	1k5			~
		R27	1k			~
		R28	4k7			~
		R30	4k7			~
		R31	470			~
		R32	47			~
		R33	150			~
		R34	68			~
		R35	27 (2W)	2W, Metalloxid		~
		R36	560			~
		R37	15k			~
		R38	470			~
		R39	15k			~
		R40	4k7			~
		R41	4k7			~
		R42	1k			~
		R43	390		T6 Gatevorspannung sollte bei etwa -1,5 Volt liegen	~
		R44	1k5			~
		R45	27k			~
		R46	27k			~
		R47	27k			~
		R48	27k			~
		R49	27k			~
		R50	27k			~
		SW1	Rotary_Encoder_Switch			~
		T1	BC547			~
		T2	BC557			~
		T3	BD138			~
		T4	BC547			~
		T5	BC547			~
		T6	CLY5	SMD		~
		T7	BC547			~
		X1	10,245MHz		Kurzschluss vermeiden, Abstand mit Papierstreifen!	~
			PCB		2 diagonale Ecken an Gehäuse anpassen, alle Kanten ein paar 10-tel abschleifen,	
					6-8 mm über dem Bodendeckel, nicht zu hoch, wegen IC11	FA 7/17 S, 661
IC-socket			28 pin narrow			
1n feed through C's	21				Gehäuse bohren, siehe Text (12 mm vom Bodendeckel)	assemblymanual4.0.pdf
tin box			100*160*30 mm			
Collated Components:						
Item	Qty	Reference(s)	Value	Remark		Datasheet
1	6	C1, C17, C55, C57, C62, C64	47			~
2	16	C2, C3, C7, C36, C38, C40, C44, C45, C48, C49, C53, C54, C56, C58, C61, C63	1n			~

3	3	C4, C5, C6	0,5-5p			~
4	1	C8	10p			~
5	1	C9	56p			~
6	1	C10	1p			~
7	2	C11, C12	15p			~
8	8	C13, C14, C32, C35, C41, C42, C50, C74	10n			~
9	1	C15	100			~
10	1	C16	120			~
11	18	C18, C19, C20, C21, C22, C23, C25, C26, C30, C31, C33, C34, C46, C52, C65, C67, C69, C70	100n			~
12	1	C24	33n			~
13	1	C27	220n			~
14	1	C28	220u			~
15	1	C29	47n			~
16	1	C37	680n			~
17	6	C39, C43, C47, C51, C66, C68	47u			~
18	1	C60	see text			~
19	3	C71, C72, C73	3,3n			~
20	2	D1, D2	BAT63-03W			~
21	1	D3	Z5v6			~
22	1	D4	Z5V6			~
23	1	D5	C3V3			~
24	3	D6, D7, D8	1N4148			~
25	1	F1	SFE10,7MT			
26	1	F2	CFU455D			
27	2	IC1, IC13	MAR-8			
28	1	IC2	IAM-81008			
29	1	IC3	80MHz			
30	1	IC4	MC3362			
31	1	IC5	LM386-3			
32	1	IC6	LM741			http://www.ti.com/lit/ds/symlink/lm741.pdf
33	1	IC7	VCO			
34	1	IC8	ADF4153			
35	1	IC9	TCXO-13MHz			
36	1	IC10	L78L09			http://www.st.com/content/ccc/resource/technical/document/datasheet/15/55/e5/aa/23/5b/43/fd/CD00000446.pdf/files/CD00000446.pdf/cr-content/translations/en/CD00000446.pdf
37	1	IC11	LM7805			http://www.fairchildsemi.com/ds/LM/LM7805.pdf
38	1	IC12	ATMega328P			
39	1	IC14	AH-1			
40	1	IC15	78L33			http://www.ti.com/lit/ds/symlink/lm3480.pdf
41	1	IC16	LCD			http://www.newhavendisplay.com/specs/NHD-0420H1Z-FSW-GBW-33V3.pdf
42	1	J1	AVR-ISP-6			~
43	3	L1, L5, L6	8wnd-1,5mm			~
44	2	L2, L3	300nH			
45	1	L4	455kHz			
46	1	LS1	Speaker			~
47	1	MK1	Microphone			~
48	1	P1	100k			~
49	1	P2	50k			~
50	1	P3	100k			~
51	1	P4	25k			~
52	2	R1, R14	10			~
53	2	R2, R33	150			~
54	3	R3, R31, R38	470			~
55	2	R4, R6	100			~
56	2	R5, R34	68			~
57	3	R7, R8, R32	47			~
58	6	R9, R10, R22, R23, R27, R42	1k			~
59	1	R11	33k			~
60	2	R12, R13	10k			~
61	1	R15	12			~
62	2	R16, R17	150k			~
63	3	R18, R26, R44	1k5			~
64	3	R19, R37, R39	15k			~
65	1	R20	2M2			~
66	1	R24	100k			~
67	4	R28, R30, R40, R41	4k7			~
68	1	R35	27 (2W)			~
69	1	R36	560			~

70	1	R43	390			~
71	6	R45, R46, R47, R48, R49, R50	27k			~
72	1	SW1	Rotary_Encoder_Switch			~
73	4	T1, T4, T5, T7	BC547			~
74	1	T2	BC557			~
75	1	T3	BD138			~
76	1	T6	CLY5			
82	1	X1	10.245MHz			~

LM3480 100-mA, SOT-23, Quasi Low-Dropout Linear Voltage Regulator

1 Features

- Input Voltage Range: up to 30 V
- 3.3-V, 5-V, 12-V, and 15-V Versions Available
- Packaged in the Tiny 3-Lead SOT-23 Package
- 30-V Maximum Input for Operation
- 1.2-V Ensured Maximum Dropout Over Full Load and Temperature Ranges
- 100-mA Ensured Minimum Load Current
- $\pm 5\%$ Ensured Output Voltage Tolerance Over Full Load and Temperature Ranges
- -40 to $+125^{\circ}\text{C}$ Junction Temperature Range for Operation

2 Applications

- Tiny Alternative to LM78Lxx Series and Similar Devices
- Tiny 5-V $\pm 5\%$ to 3.3-V, 100-mA Converter
- Post Regulator for Switching DC/DC Converter
- Bias Supply for Analog Circuits

3 Description

The LM3480 is an integrated linear voltage regulator. It features operation from an input as high as 30 V and an ensured maximum dropout of 1.2 V at the full 100-mA load. Standard packaging for the LM3480 is the 3-lead SOT-23 package.

The 5-V, 12-V, and 15-V members of the LM3480 series are intended as tiny alternatives to industry standard LM78Lxx series and similar devices. The 1.2-V quasi-low dropout of LM3480 series devices makes them a nice fit in many applications where the 2-V to 2.5-V dropout of LM78Lxx series devices precludes their (LM78Lxx series devices) use.

The LM3480 series also features a 3.3-V member. The SOT-23 packaging and quasi-low dropout features of the LM3480 series converge in this device to provide a very nice, very tiny, 3.3-V, 100-mA bias supply that regulates directly off the system 5-V $\pm 5\%$ power supply.

Device Information⁽¹⁾

PART NUMBER	PACKAGE	BODY SIZE (NOM)
LM3480	SOT-23 (3)	2.92 mm \times 1.30 mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Typical Application Circuit

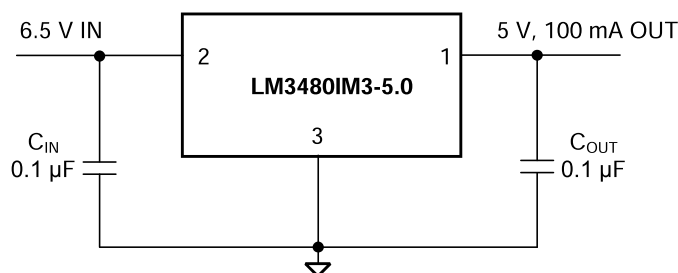


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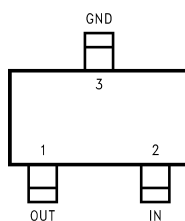
4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision G (February 2015) to Revision H	Page
• Replaced <i>Functional Block Diagram</i>	10
• Changed text of <i>External Capacitors</i> subsection	11
• Changed text of <i>Output Capacitor</i> subsection	11
Changes from Revision F (December 2014) to Revision G	Page
• Changed pin numbers indicated in <i>Typical Application</i> drawing; fix typos	1
• Deleted soldering specs - found in POA	4
• Changed <i>Handling Ratings</i> to <i>ESD Ratings</i> format	4
Changes from Revision E (March 2013) to Revision F	Page
• Added <i>Pin Configuration and Functions</i> section, <i>Handling Rating</i> table, <i>Feature Description</i> section, <i>Device Functional Modes</i> , <i>Application and Implementation</i> section, <i>Power Supply Recommendations</i> section, <i>Layout</i> section, <i>Device and Documentation Support</i> section, and <i>Mechanical, Packaging, and Orderable Information</i> section; add updated <i>Thermal Information</i>	1
Changes from Revision D (March 2013) to Revision E	Page
• Changed layout of National Data Sheet to TI format	9

5 Pin Configuration and Functions

**DBZ Package
3-Pin SOT-23
Top View**



Pin Functions

PIN		I/O	DESCRIPTION
NAME	NO.		
OUT	1	O	Output voltage
IN	2	I	Input voltage supply
GND	3	—	Common ground

6 Specifications

6.1 Absolute Maximum Ratings⁽¹⁾⁽²⁾

	MIN	MAX	UNIT
Input voltage (IN to GND)	–0.3	35	V
Power dissipation ⁽³⁾		Internally Limited	
Junction temperature ⁽³⁾	–40	150	°C
Storage temperature, T _{stg}	–65	150	°C

- (1) *Absolute Maximum Ratings* are limits beyond which damage to the device may occur. *Recommended Operating Conditions* are conditions under which operation of the device is ensured. Recommended operating ratings do not imply ensured performance limits. For ensured performance limits and associated test conditions, see the [Electrical Characteristics: LM3480-3.3, LM3480-5](#).
- (2) If Military- or Aerospace-specified devices are required, please contact the TI Sales Office/Distributors for availability and specifications.
- (3) The Absolute Maximum power dissipation depends on the ambient temperature and can be calculated using $P = (T_J - T_A) / R_{\theta JA}$ where T_J is the junction temperature, T_A is the ambient temperature, and $R_{\theta JA}$ is the junction-to-ambient thermal resistance. The 370-mW rating results from substituting the Absolute Maximum junction temperature, 150°C for T_J , 50°C for T_A , and 269.6°C/W for $R_{\theta JA}$. More power can be safely dissipated at lower ambient temperatures. Less power can be safely dissipated at higher ambient temperatures. The Absolute Maximum power dissipation can be increased by 3.7 mW for each °C below 50°C ambient. It must be derated by 3.7 mW for each °C above 50°C ambient. Heat sinking enables the safe dissipation of more power. The LM3480 actively limits its junction temperature to about 150°C.

6.2 ESD Ratings

	VALUE	UNIT
V _(ESD) Electrostatic discharge	Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 ⁽¹⁾	±2000
	Charged-device model (CDM), per JEDEC specification JESD22-C101 ⁽²⁾	±500

- (1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
- (2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)⁽¹⁾

	MIN	MAX	UNIT
Maximum input voltage (IN to GND)	0	30	V
Junction temperature (T _J)	–40	125	°C

- (1) *Absolute Maximum Ratings* are limits beyond which damage to the device may occur. *Recommended Operating Conditions* are conditions under which operation of the device is ensured. Recommended operating ratings do not imply ensured performance limits. For ensured performance limits and associated test conditions, see the [Electrical Characteristics: LM3480-3.3, LM3480-5](#).

6.4 Thermal Information

THERMAL METRIC ⁽¹⁾		LM3480	UNIT
		SOT-23 (DBZ)	
		3 PINS	
R _{θJA}	Junction-to-ambient thermal resistance	269.6	°C/W
R _{θJC(top)}	Junction-to-case (top) thermal resistance	141.1	
R _{θJB}	Junction-to-board thermal resistance	63.1	
ψ _{JT}	Junction-to-top characterization parameter	24.2	
ψ _{JB}	Junction-to-board characterization parameter	62.1	

- (1) For more information about traditional and new thermal metrics, see the *IC Package Thermal Metrics* application report, [SPRA953](#).

6.5 Electrical Characteristics: LM3480-3.3, LM3480-5

Typical and other limits apply for $T_A = T_J = 25^\circ\text{C}$, unless otherwise specified. Nominal output voltage (V_{NOM}) = 3.3 V or 5 V. ⁽¹⁾⁽²⁾⁽³⁾

PARAMETER	TEST CONDITIONS	$V_{\text{NOM}} = 3.3 \text{ V}$			$V_{\text{NOM}} = 5 \text{ V}$			UNIT
		MIN	TYP	MAX	MIN	TYP	MAX	
V_{OUT} Output voltage	$V_{\text{IN}} = V_{\text{NOM}} + 1.5 \text{ V}$ $1 \text{ mA} \leq I_{\text{OUT}} \leq 100 \text{ mA}$	3.17	3.3	3.43	4.8	5	5.2	V
	$V_{\text{IN}} = V_{\text{NOM}} + 1.5 \text{ V}$ $1 \text{ mA} \leq I_{\text{OUT}} \leq 100 \text{ mA}$ $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$	3.14		3.46	4.75		5.25	
ΔV_{OUT} Line regulation	$V_{\text{NOM}} + 1.5 \text{ V} \leq V_{\text{IN}} \leq 30 \text{ V}$ $I_{\text{OUT}} = 1 \text{ mA}$		10			12		mV
	$V_{\text{NOM}} + 1.5 \text{ V} \leq V_{\text{IN}} \leq 30 \text{ V}$ $I_{\text{OUT}} = 1 \text{ mA}$ $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			25			25	
ΔV_{OUT} Load regulation	$V_{\text{IN}} = V_{\text{NOM}} + 1.5 \text{ V}$ $10 \text{ mA} \leq I_{\text{OUT}} \leq 100 \text{ mA}$		20			20		mV
	$V_{\text{IN}} = V_{\text{NOM}} + 1.5 \text{ V}$ $10 \text{ mA} \leq I_{\text{OUT}} \leq 100 \text{ m}$ $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			40			40	
I_{GND} Ground pin current	$V_{\text{NOM}} + 1.5 \text{ V} \leq V_{\text{IN}} \leq 30 \text{ V}$ No Load		2			2		mA
	$V_{\text{NOM}} + 1.5 \text{ V} \leq V_{\text{IN}} \leq 30 \text{ V}$ No Load, $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			4			4	
$V_{\text{IN}} - V_{\text{OUT}}$ Dropout voltage	$I_{\text{OUT}} = 10 \text{ mA}$		0.7	0.9		0.7	0.9	V
	$I_{\text{OUT}} = 10 \text{ mA}$ $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			1			1	
	$I_{\text{OUT}} = 100 \text{ mA}$		0.9	1.1		0.9	1.1	V
	$I_{\text{OUT}} = 100 \text{ mA}$ $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			1.2			1.2	
e_n Output noise voltage	$V_{\text{IN}} = 10 \text{ V}$ Bandwidth: 10 Hz to 100 kHz		100			150		μV_{rms}

- (1) A typical is the center of characterization data taken with $T_A = T_J = 25^\circ\text{C}$. Typical values are not ensured.
- (2) All limits are ensured. All electrical characteristics having room-temperature limits are tested during production with $T_A = T_J = 25^\circ\text{C}$. All hot and cold limits are ensured by correlating the electrical characteristics to process and temperature variations and applying statistical process control.
- (3) All voltages except dropout are with respect to the voltage at the GND pin.

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6.6 Electrical Characteristics: LM3480-12, LM3480-15

Typical and other limits apply for $T_A = T_J = 25^\circ\text{C}$, unless otherwise specified. Nominal output voltage (V_{NOM}) = 12 V or 15 V.⁽¹⁾⁽²⁾⁽³⁾

PARAMETER	TEST CONDITIONS	$V_{\text{NOM}} = 12\text{ V}$			$V_{\text{NOM}} = 15\text{ V}$			UNIT
		MIN	TYP	MAX	MIN	TYP	MAX	
V_{OUT} Output voltage	$V_{\text{IN}} = V_{\text{NOM}} + 1.5\text{ V}$ $1\text{ mA} \leq I_{\text{OUT}} \leq 100\text{ mA}$	11.52	12	12.48	14.4	15	15.6	V
	$V_{\text{IN}} = V_{\text{NOM}} + 1.5\text{ V}$ $1\text{ mA} \leq I_{\text{OUT}} \leq 100\text{ mA}$ $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$	11.4		12.6	14.25		15.75	
ΔV_{OUT} Line regulation	$V_{\text{NOM}} + 1.5\text{ V} \leq V_{\text{IN}} \leq 30\text{ V}$ $I_{\text{OUT}} = 1\text{ mA}$		14			16		mV
	$V_{\text{NOM}} + 1.5\text{ V} \leq V_{\text{IN}} \leq 30\text{ V}$ $I_{\text{OUT}} = 1\text{ mA}$ $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			40			40	
ΔV_{OUT} Load regulation	$V_{\text{IN}} = V_{\text{NOM}} + 1.5\text{ V}$ $10\text{ mA} \leq I_{\text{OUT}} \leq 100\text{ mA}$		36			45		mV
	$V_{\text{IN}} = V_{\text{NOM}} + 1.5\text{ V}$ $10\text{ mA} \leq I_{\text{OUT}} \leq 100\text{ mA}$ $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			60			75	
I_{GND} Ground pin current	$V_{\text{NOM}} + 1.5\text{ V} \leq V_{\text{IN}} \leq 30\text{ V}$ No Load		2			2		mA
	$V_{\text{NOM}} + 1.5\text{ V} \leq V_{\text{IN}} \leq 30\text{ V}$ No Load, $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			4			4	
$V_{\text{IN}} - V_{\text{OUT}}$ Dropout voltage	$I_{\text{OUT}} = 10\text{ mA}$		0.7	0.9		0.7	0.9	V
	$I_{\text{OUT}} = 10\text{ mA}$, $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			1			1	
	$I_{\text{OUT}} = 100\text{ mA}$		0.9	1.1		0.9	1.1	V
	$I_{\text{OUT}} = 100\text{ mA}$, $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$			1.2			1.2	
e_n Output noise voltage	$V_{\text{IN}} = 10\text{ V}$ Bandwidth: 10 Hz to 100 kHz		360			450		μV_{rms}

- (1) A typical is the center of characterization data taken with $T_A = T_J = 25^\circ\text{C}$. Typical values are not ensured.
- (2) All limits are ensured. All electrical characteristics having room-temperature limits are tested during production with $T_A = T_J = 25^\circ\text{C}$. All hot and cold limits are ensured by correlating the electrical characteristics to process and temperature variations and applying statistical process control.
- (3) All voltages except dropout are with respect to the voltage at the GND pin.

6.7 Typical Characteristics

Unless indicated otherwise, $V_{IN} = V_{NOM} + 1.5\text{ V}$, $C_{IN} = 0.1\text{ }\mu\text{F}$, $C_{OUT} = 0.1\text{ }\mu\text{F}$, and $T_A = 25^\circ\text{C}$.

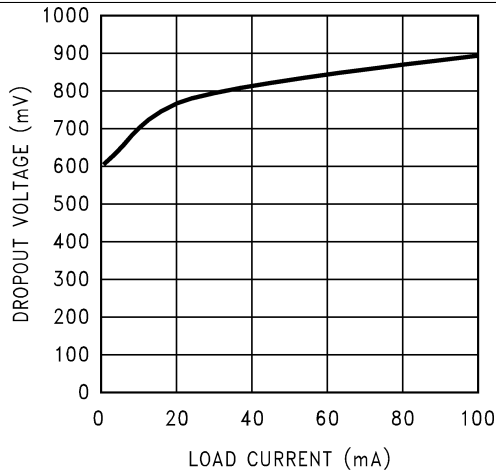


Figure 1. Dropout Voltage vs Load Current

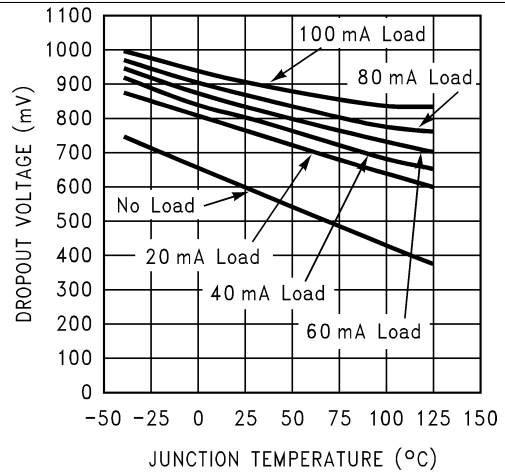


Figure 2. Dropout Voltage vs Junction Temperature

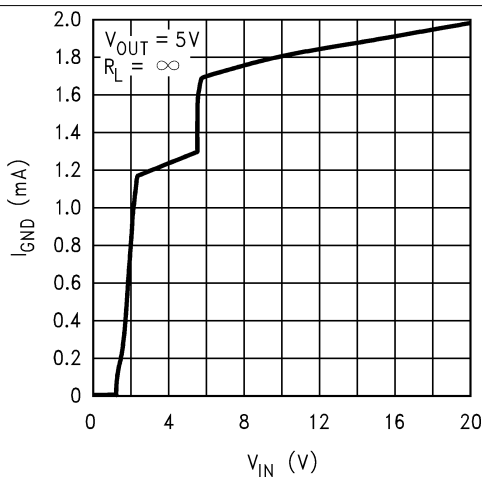


Figure 3. Ground Pin Current vs Input Voltage

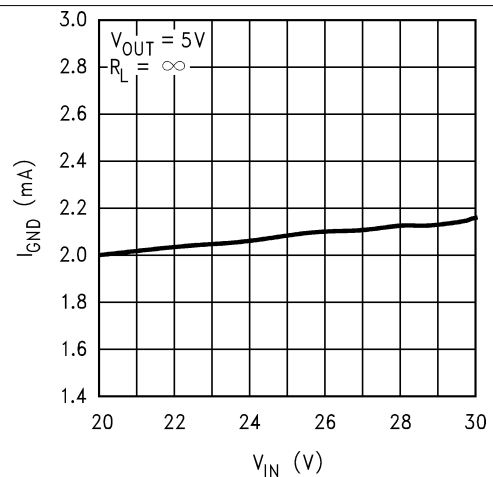


Figure 4. Ground Pin Current vs Input Voltage

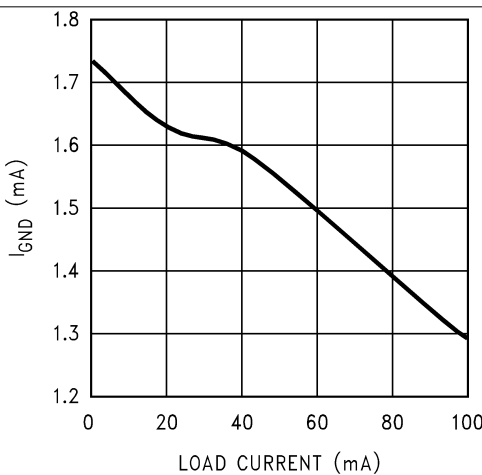


Figure 5. Ground Pin Current vs Load Current

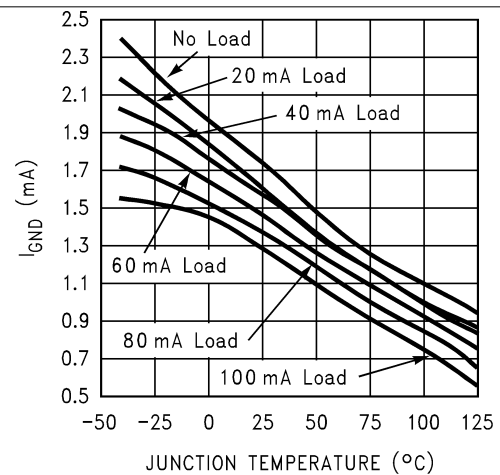


Figure 6. Ground Pin Current vs Junction Temperature

Typical Characteristics (continued)

Unless indicated otherwise, $V_{IN} = V_{NOM} + 1.5\text{ V}$, $C_{IN} = 0.1\text{ }\mu\text{F}$, $C_{OUT} = 0.1\text{ }\mu\text{F}$, and $T_A = 25^\circ\text{C}$.

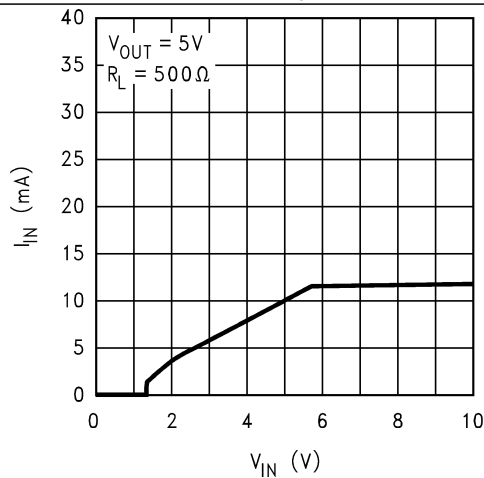


Figure 7. Input Current vs Input Voltage

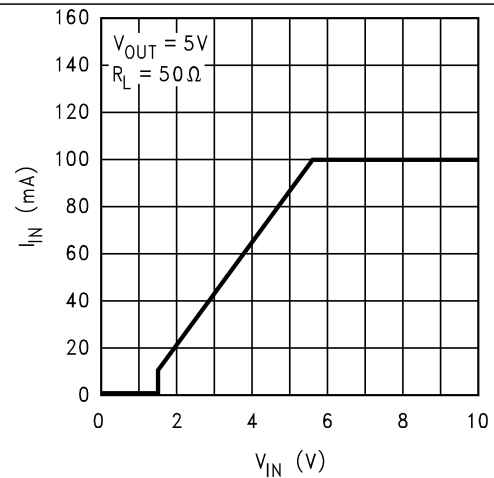


Figure 8. Input Current vs Input Voltage

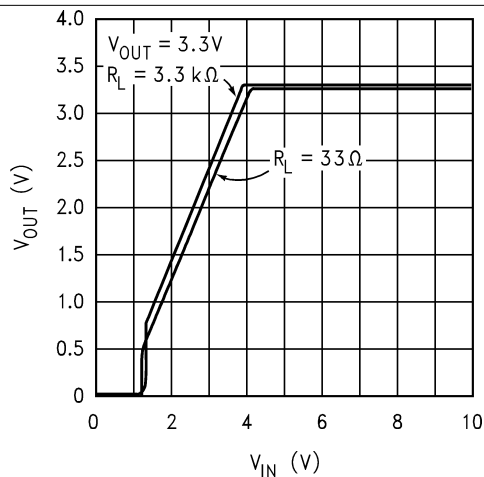


Figure 9. Output Voltage vs Input Voltage

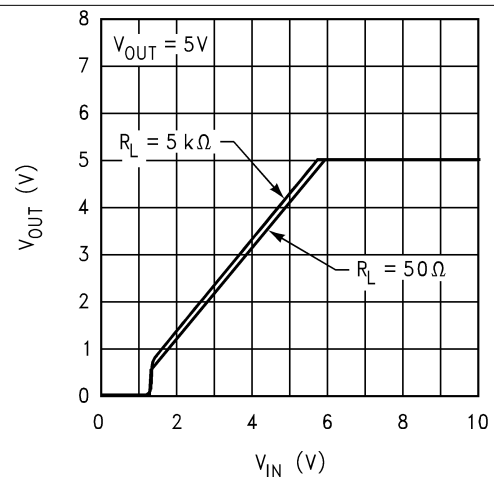


Figure 10. Output Voltage vs Input Voltage

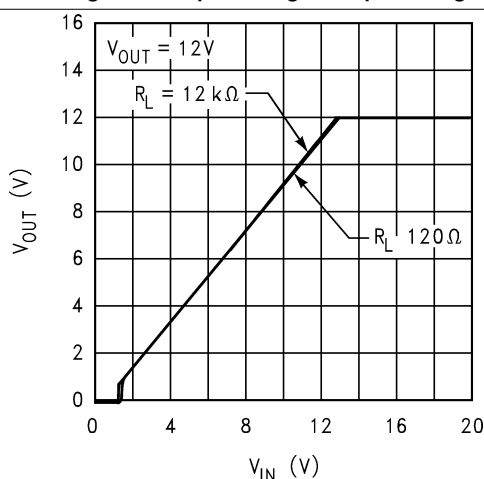


Figure 11. Output Voltage vs Input Voltage

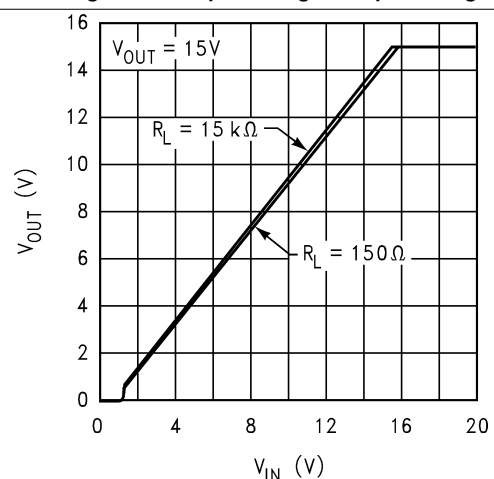


Figure 12. Output Voltage vs Input Voltage

Typical Characteristics (continued)

Unless indicated otherwise, $V_{IN} = V_{NOM} + 1.5\text{ V}$, $C_{IN} = 0.1\text{ }\mu\text{F}$, $C_{OUT} = 0.1\text{ }\mu\text{F}$, and $T_A = 25^\circ\text{C}$.

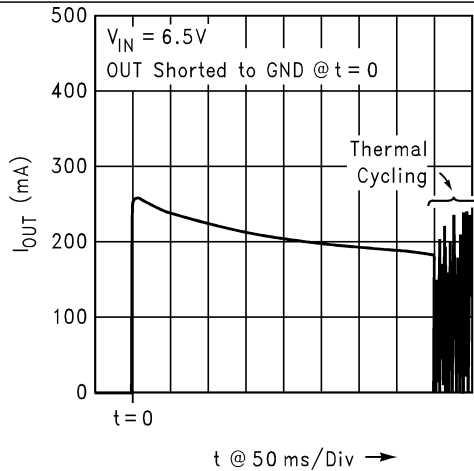


Figure 13. Output Short-Circuit Current

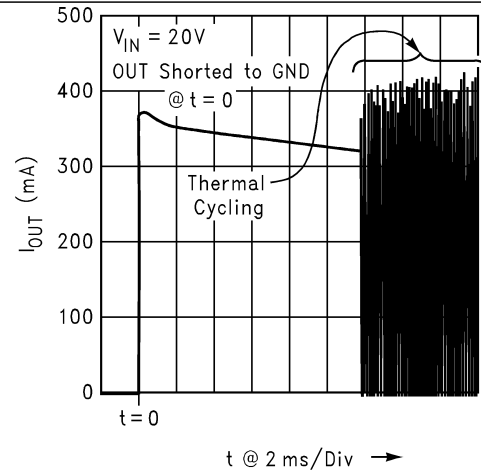


Figure 14. Output Short-Circuit Current

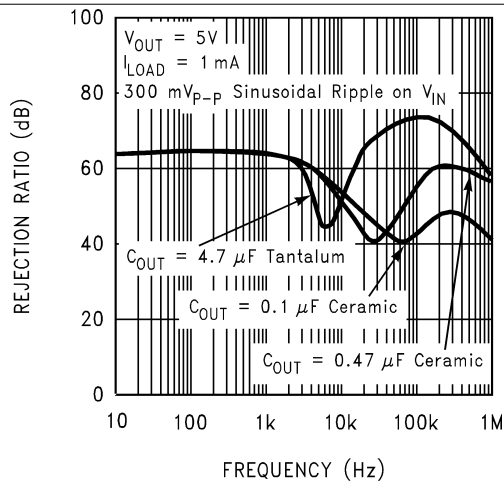


Figure 15. Power Supply Rejection Ratio

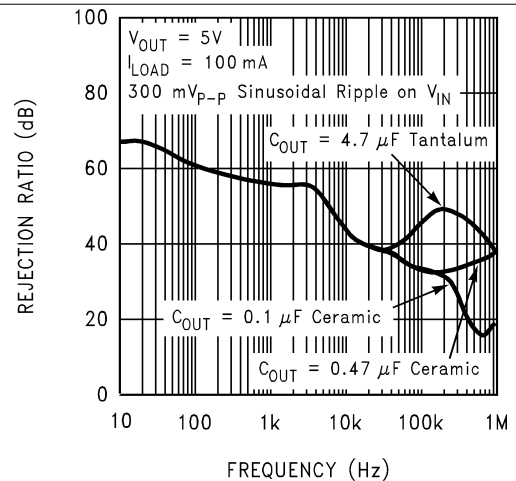


Figure 16. Power Supply Rejection Ratio

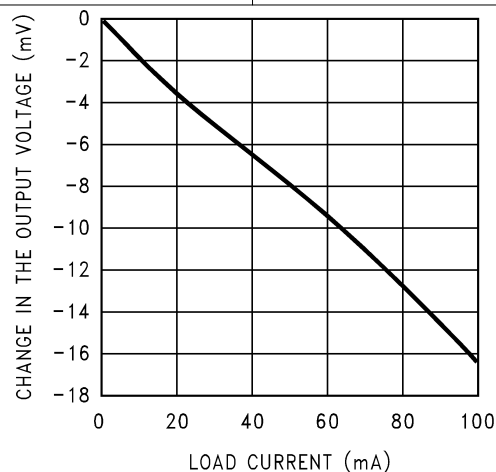


Figure 17. DC Load Regulation

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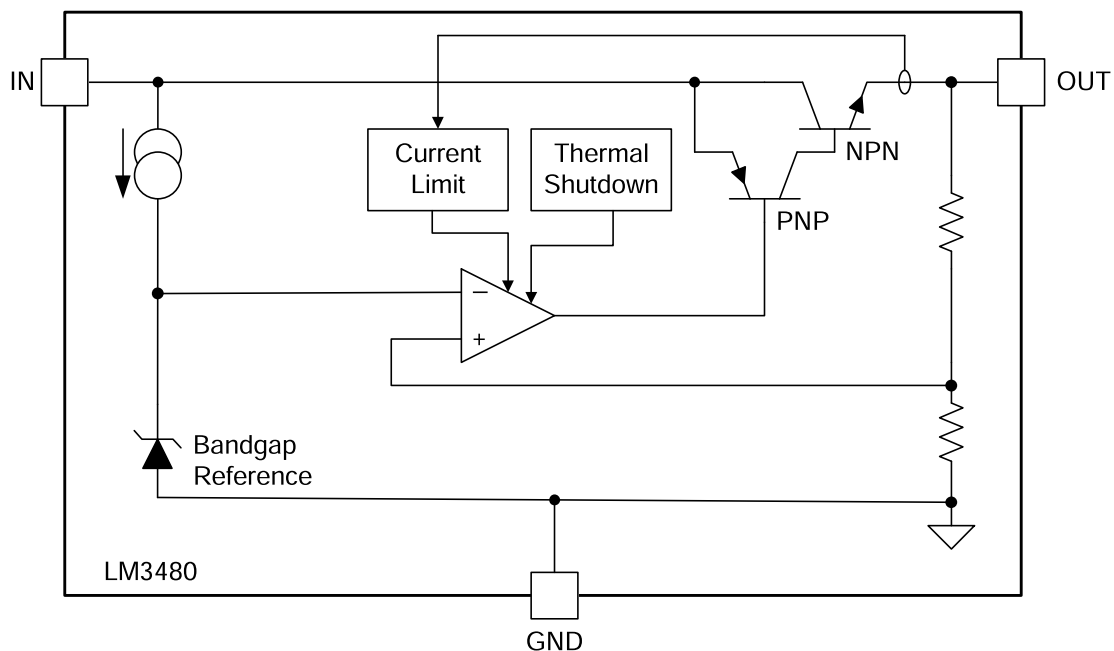
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7 Detailed Description

7.1 Overview

The LM3480 is an integrated linear voltage regulator with inputs that can be as high as 30 V. It ensures a maximum dropout of 1.2 V at the full load of 100 mA. The LM3480 has different output options including 3.3-V, 5-V, 12-V, and 15-V outputs, making LM3480 the tiny alternative to industry standard LM78Lxx series and similar devices.

7.2 Functional Block Diagram



7.3 Feature Description

7.3.1 3.3-V, 5-V, 12-V, and 15-V Versions Available

The 3.3-V, 5-V, 12-V, and 15-V versions of LM3480 series are intended as tiny alternatives to industry standard LM78Lxx series and similar devices.

7.3.2 1.2-V Ensured Maximum Dropout

The 1.2-V quasi-low dropout of the LM3480 series devices make them a nice fit in many application where the 2-V to 2.5-V dropout of LM78Lxx series devices precludes their use.

7.4 Device Functional Modes

7.4.1 Operation with $V_{IN} = 5\text{ V}$

The 3.3-V member of LM3480 can operate with an input of $5\text{ V} \pm 5\%$, its tiny SOT-23 package and quasi-low dropout makes it suitable for providing a very tiny, 3.3-V, 100-mA bias supply from 5-V power supply.

8 Application and Implementation

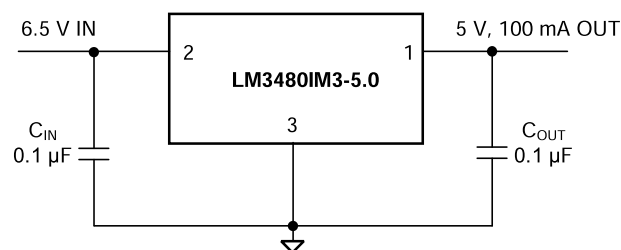
NOTE

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

8.1 Application Information

The LM3480 is a linear voltage regulator with 1.2-V ensured maximum dropout and 100-mA ensured minimum load current. This device has 3.3-V, 5-V, 12-V, and 15-V versions. The implementation of LM3480 is discussed in this section.

8.2 Typical Application



8.2.1 Design Requirements

DESIGN PARAMETER	EXAMPLE VALUE
Input voltage	6.5 V
Output voltage	5 V
Output current	100 mA

8.2.2 Detailed Design Procedure

8.2.2.1 External Capacitors

A minimum input and output capacitance value of 0.1 μF is required for stability and adequate transient performance. There is no specific ESR limitation, although excessively high ESR will compromise transient performance. There is no specific limitation on a maximum capacitance value on the input or the output.

8.2.2.1.1 Output Capacitor

The minimum output capacitance required to maintain stability is 0.1 μF . Larger values of output capacitance can be used to improve transient behavior.

8.2.3 Application Curves

Unless indicated otherwise, $V_{\text{IN}} = 6.5 \text{ V}$, $V_{\text{OUT}} = 5 \text{ V}$, $C_{\text{OUT}} = 0.1 \mu\text{F}$, and $T_A = 25^\circ\text{C}$

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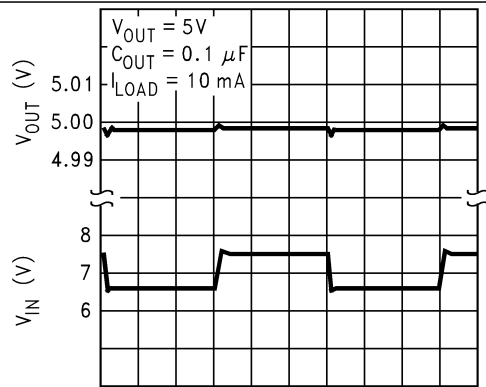


Figure 18. Line Transient Response

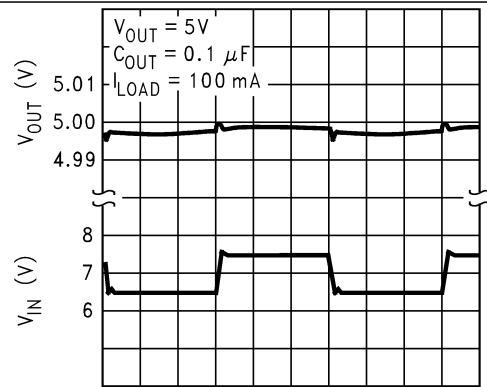


Figure 19. Line Transient Response

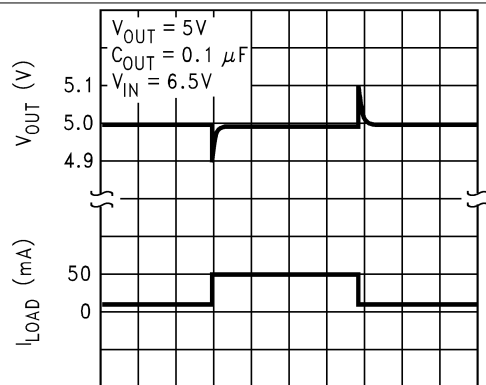


Figure 20. Load Transient Response

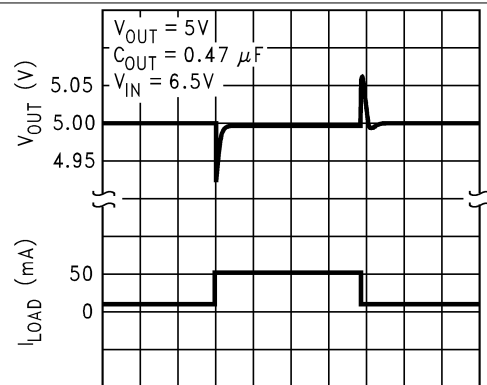


Figure 21. Load Transient Response

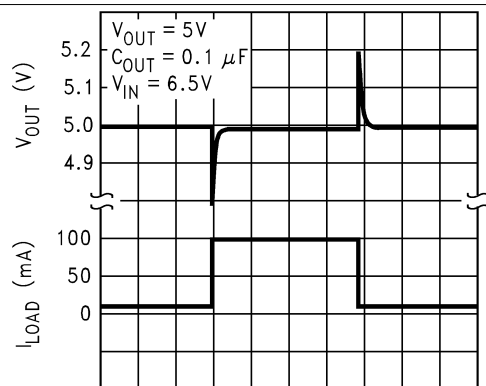


Figure 22. Load Transient Response

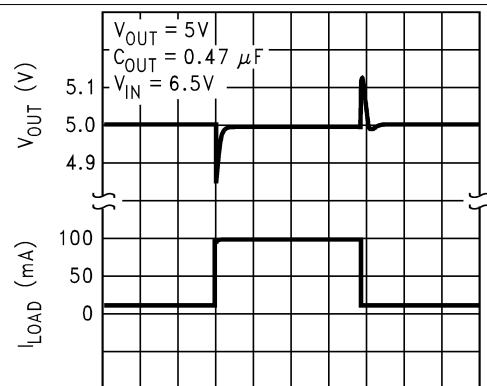


Figure 23. Load Transient Response

9 Power Supply Recommendations

The LM3480 is designed to operate from up to a 30-V input voltage supply. This input supply must be well regulated. If the input supply is noisy, additional input capacitors with low ESR can help to improve the output noise performance.

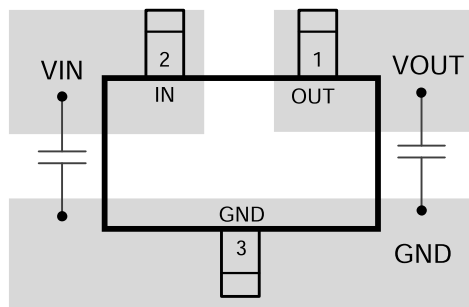
10 Layout

10.1 Layout Guidelines

For best overall performance, place all the circuit components on the same side of the circuit board and as near as practical to the respective LDO pin connections. Place ground return connections to the input and output capacitors, and to the LDO ground pin as close to each other as possible, connected by a wide, component-side, copper surface. The use of vias and long traces to create LDO circuit connections is strongly discouraged and negatively affects system performance. This grounding and layout scheme minimizes the inductive parasitic, and thereby reduces load-current transients, minimizes noise, and increases circuit stability.

A ground reference plane is also recommended and is either embedded in the PCB itself or located on the bottom side of the PCB opposite the components. This reference plane serves to assure accuracy of the output voltage, shield noise, and behaves similar to a thermal plane to spread heat from the LDO device. In most applications, this ground plane is necessary to meet thermal requirements.

10.2 Layout Example



11 Device and Documentation Support

11.1 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

TI E2E™ Online Community *TI's Engineer-to-Engineer (E2E) Community*. Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

Design Support *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

11.2 Trademarks

E2E is a trademark of Texas Instruments.

All other trademarks are the property of their respective owners.

11.3 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

11.4 Glossary

[SLYZ022](#) — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

12 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM3480IM3-12	NRND	SOT-23	DBZ	3	1000	Non-RoHS & Green	Call TI	Level-1-260C-UNLIM	-40 to 125	L0C	
LM3480IM3-12/NOPB	ACTIVE	SOT-23	DBZ	3	1000	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 125	L0C	Samples
LM3480IM3-15/NOPB	ACTIVE	SOT-23	DBZ	3	1000	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 125	L0D	Samples
LM3480IM3-3.3	NRND	SOT-23	DBZ	3	1000	Non-RoHS & Green	Call TI	Level-1-260C-UNLIM	-40 to 125	L0A	
LM3480IM3-3.3/NOPB	ACTIVE	SOT-23	DBZ	3	1000	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 125	L0A	Samples
LM3480IM3-5.0	NRND	SOT-23	DBZ	3	1000	Non-RoHS & Green	Call TI	Level-1-260C-UNLIM	-40 to 125	L0B	
LM3480IM3-5.0/NOPB	ACTIVE	SOT-23	DBZ	3	1000	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 125	L0B	Samples
LM3480IM3X-12/NOPB	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 125	L0C	Samples
LM3480IM3X-15/NOPB	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 125	L0D	Samples
LM3480IM3X-3.3	NRND	SOT-23	DBZ	3	3000	Non-RoHS & Green	Call TI	Level-1-260C-UNLIM	-40 to 125	L0A	
LM3480IM3X-3.3/NOPB	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 125	L0A	Samples
LM3480IM3X-5.0/NOPB	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 125	L0B	Samples

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

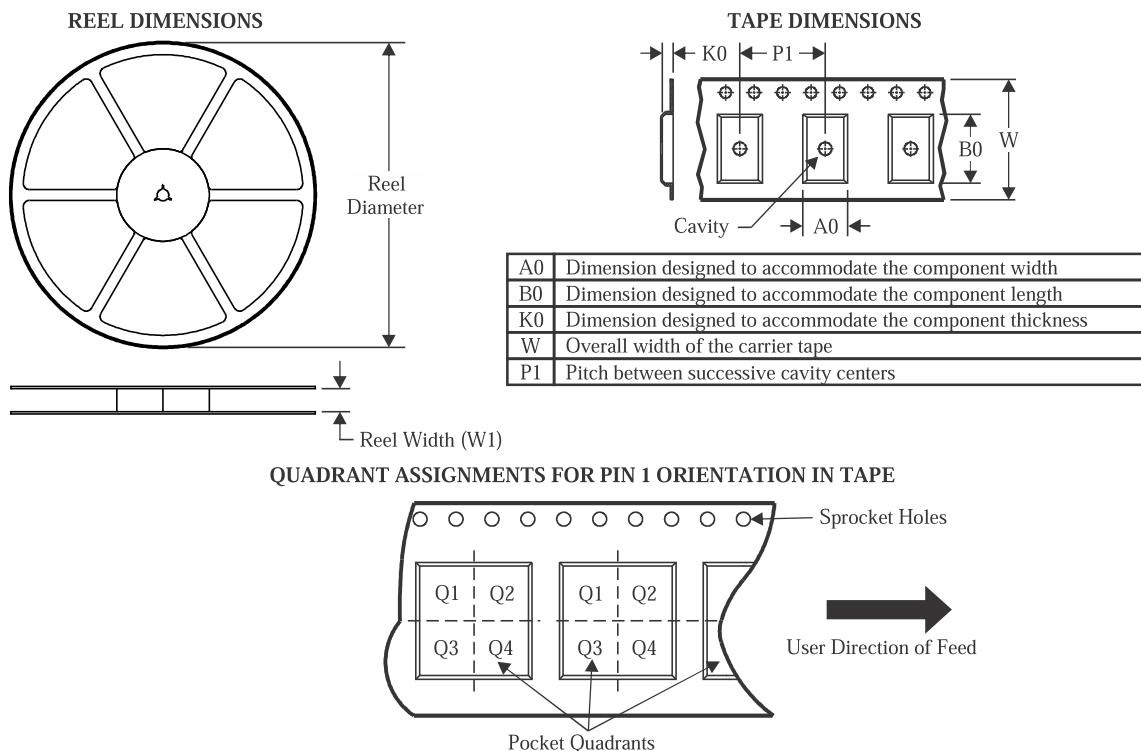
(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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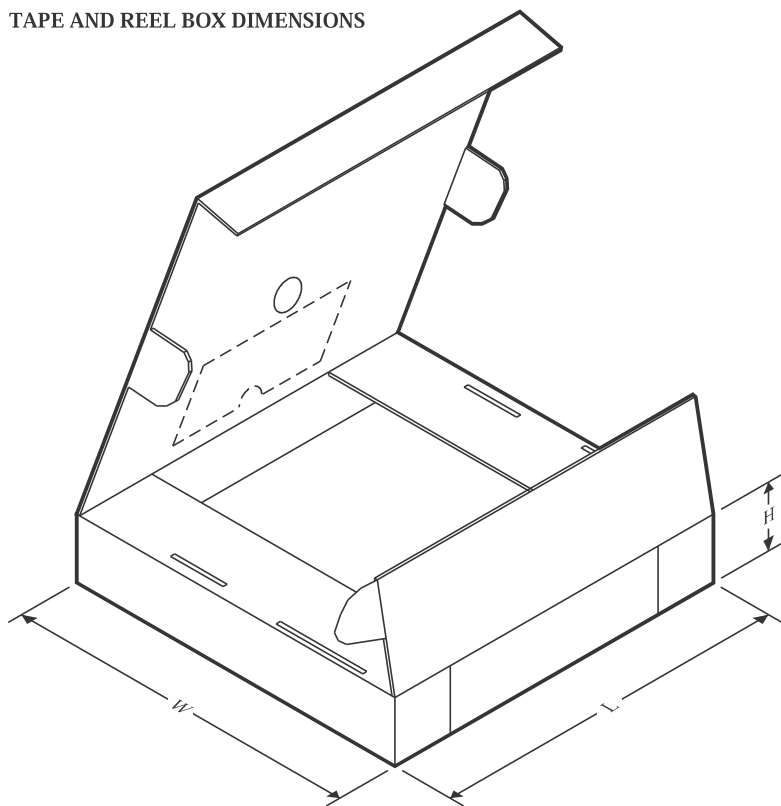
TAPE AND REEL INFORMATION



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM3480IM3-12	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3-12/NOPB	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3-15/NOPB	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3-3.3	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3-3.3/NOPB	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3-5.0	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3-5.0/NOPB	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3X-12/NOPB	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3X-15/NOPB	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3X-3.3	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3X-3.3/NOPB	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM3480IM3X-5.0/NOPB	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM3480IM3-12	SOT-23	DBZ	3	1000	208.0	191.0	35.0
LM3480IM3-12/NOPB	SOT-23	DBZ	3	1000	208.0	191.0	35.0
LM3480IM3-15/NOPB	SOT-23	DBZ	3	1000	208.0	191.0	35.0
LM3480IM3-3.3	SOT-23	DBZ	3	1000	208.0	191.0	35.0
LM3480IM3-3.3/NOPB	SOT-23	DBZ	3	1000	208.0	191.0	35.0
LM3480IM3-5.0	SOT-23	DBZ	3	1000	208.0	191.0	35.0
LM3480IM3-5.0/NOPB	SOT-23	DBZ	3	1000	208.0	191.0	35.0
LM3480IM3X-12/NOPB	SOT-23	DBZ	3	3000	208.0	191.0	35.0
LM3480IM3X-15/NOPB	SOT-23	DBZ	3	3000	208.0	191.0	35.0
LM3480IM3X-3.3	SOT-23	DBZ	3	3000	208.0	191.0	35.0
LM3480IM3X-3.3/NOPB	SOT-23	DBZ	3	3000	208.0	191.0	35.0
LM3480IM3X-5.0/NOPB	SOT-23	DBZ	3	3000	208.0	191.0	35.0

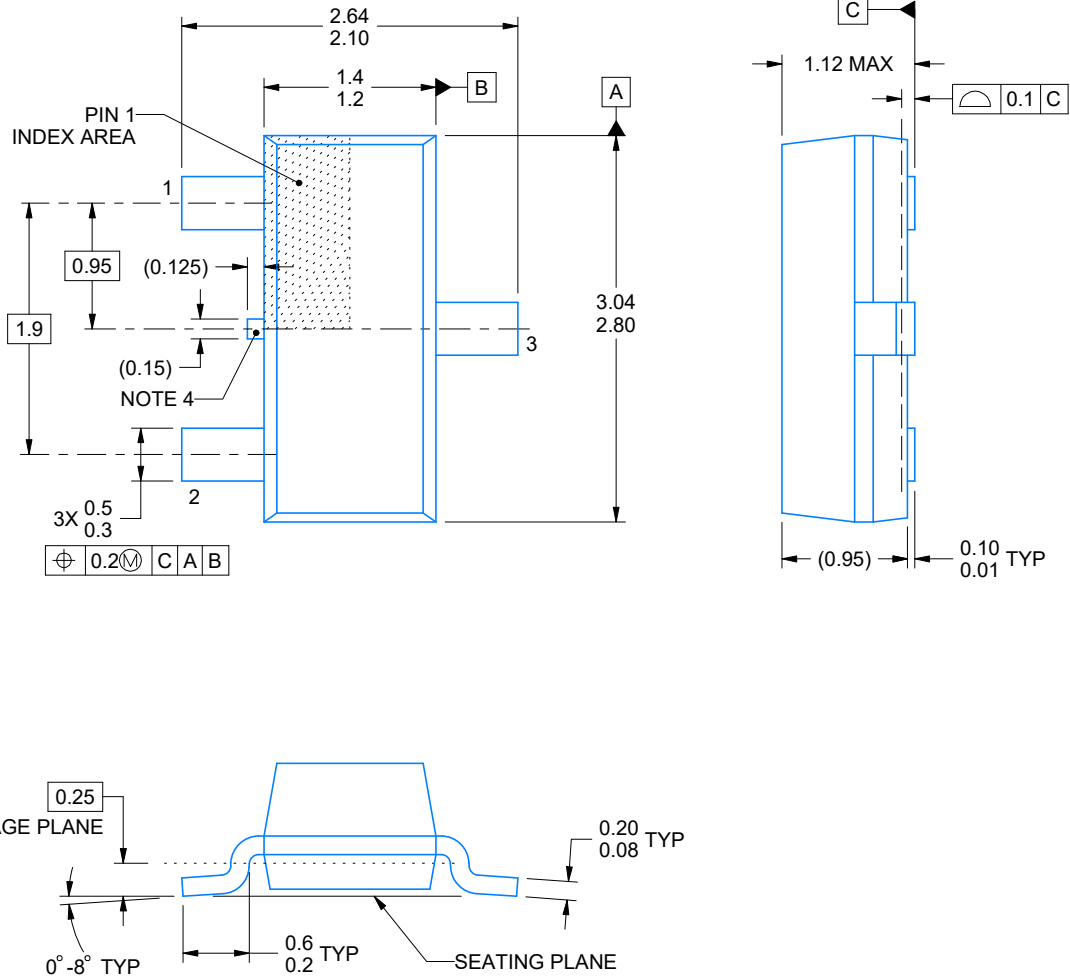
DBZ0003A



PACKAGE OUTLINE

SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



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NOTES:

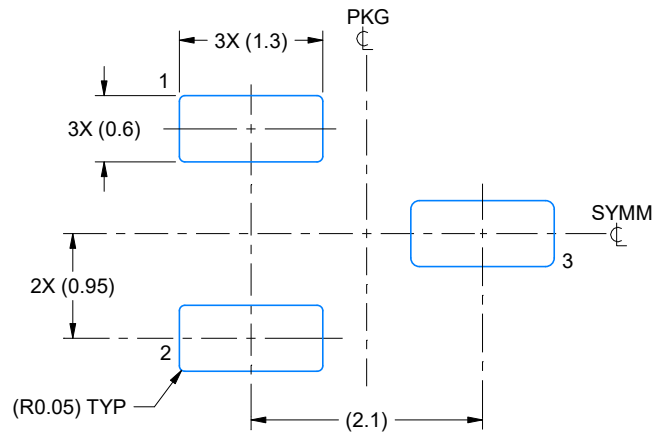
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC registration TO-236, except minimum foot length.
4. Support pin may differ or may not be present.

EXAMPLE BOARD LAYOUT

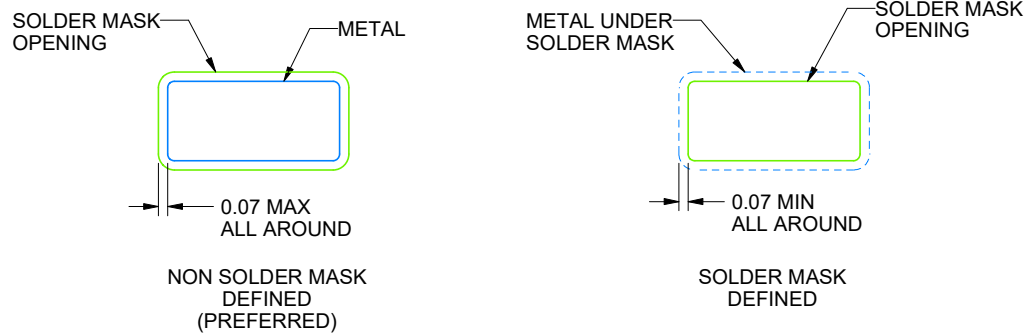
DBZ0003A

SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



LAND PATTERN EXAMPLE
SCALE:15X



SOLDER MASK DETAILS

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NOTES: (continued)

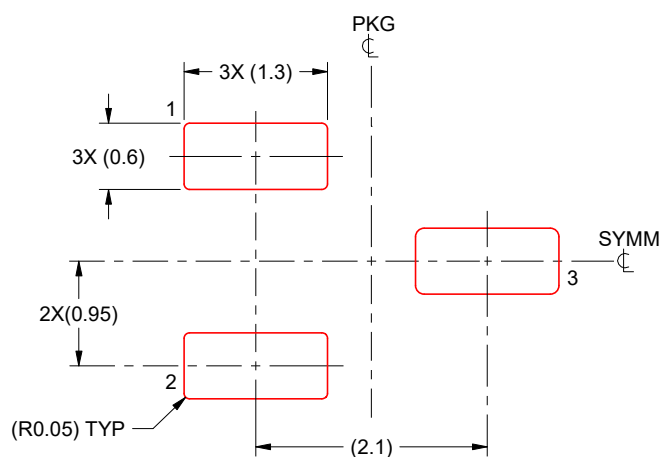
4. Publication IPC-7351 may have alternate designs.
5. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

EXAMPLE STENCIL DESIGN

DBZ0003A

SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



SOLDER PASTE EXAMPLE
BASED ON 0.125 THICK STENCIL
SCALE:15X

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NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
7. Board assembly site may have different recommendations for stencil design.

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